

Automotive gateway and domain control applications

S32G Safe and Secure Vehicle Network Processors

S32G combines hardware security, ASIL D safety, high-performance real-time and application processing, and network acceleration for service-oriented gateways, domain controllers and safety co-processors.

TARGET APPLICATIONS

- ▶ Central gateways that require acceleration for deterministic networking and processor offloading and embedded security for emerging over-the-air services.
- Service-oriented gateways that require high-performance application processing with isolation to rapidly deploy new safe and secure services.
- Domain controllers that require high-performance real-time and application processing to support ECU consolidation, network protocol translation and local domain control.
- Safety co-processors that require ASIL D functional safety processing with network connectivity and PCI Express for data sharing with other components and central mass storage.

Designed and manufactured to our proven automotive practices, and with an embedded hardware security engine (HSE), the S32G processor satisfies developers' needs for high performance, safety, security and reliability.

ENABLEMENT TOOLS

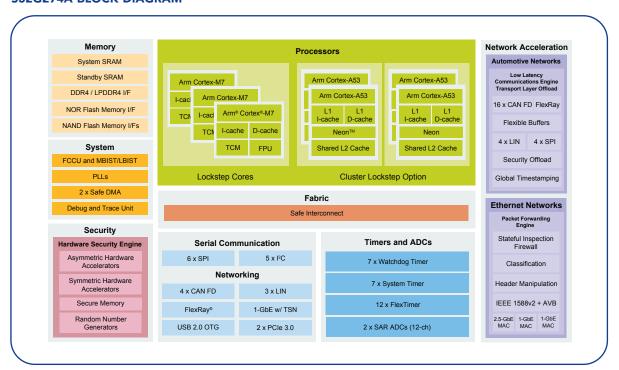
- ▶ Hardware Board Support
 - NXP S32G-VNP-EVB evaluation board
 - NXP S32G-VNP-RDB reference design board
 - Third-party single-board computer / SoM
- ▶ AUTOSAR® safe MCAL
- ▶ Linux® BSP
- ▶ S32 Design Studio IDE
- Firmware for hardware accelerators (HSE, LLCE, PFE)
- Low Latency Communications Engine Firmware Development Kit (LLCE FDK)
- Supported by POSIX-compliant operating systems, AUTOSAR Adaptive Platform, and hypervisors
- Supported by NXP S32 Debug Probe and multiple third-party debuggers
- ▶ S32 Safety Software Framework (SAF)



S32G SAFE AND SECURE VEHICLE NETWORK PROCESSOR

Key Features	Benefits
Designed and manufactured to satisfy automotive reliability and ISO 26262 ASIL D functional safety requirements	Accelerates development of functional safety applications, including advanced driver assistance systems (ADAS) and autonomous drive systems
Quad Arm® Cortex®-A53 cores with Arm Neon™ technology organized in two clusters of two cores with optional cluster lockstep	Supports high-performance processing and high-level operating systems for service-oriented gateways, ECU consolidation and safety applications
Triple Arm Cortex-M7 lockstep cores	Supports real-time processing and safety operating systems for safe processing and ECU consolidation
Low Latency Communications Engine (LLCE)	Enables deterministic automotive networks and offloads processors to focus on value-add services
Packet Forwarding Engine (PFE)	Provides high-performance stateful firewall, classification and header manipulation and offloads processors to focus on value-add services
Advanced functional safety hardware and software	Supports fail-operational fault recovery with capability through detection, isolation and resolution of faults without system shutdown
Hardware Security Engine (HSE)	Accelerates security services to offload processors and network accelerators and provides protection against IP theft and cyber security attacks
AEC-Q100 Grade 2 device (-40°C to -105°C)	Supports a wide range of automotive applications

S32G274A BLOCK DIAGRAM



SafeAssure® FUNCTIONAL SAFETY PROGRAM

Functional safety. Simplified.

Our SafeAssure functional safety program is designed to help system manufacturers more easily achieve system compliance with International Standards Organization (ISO) 26262 and International Electrotechnical Commission (IEC) 61508 functional safety standards. The program highlights our hardware and software solutions that are optimally designed to support functional safety implementations and come with a rich set of enablement collateral.



For more information, visit www.nxp.com/SafeAssure.

www.nxp.com/S32G

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